

# Interconnected Fabric Module

00

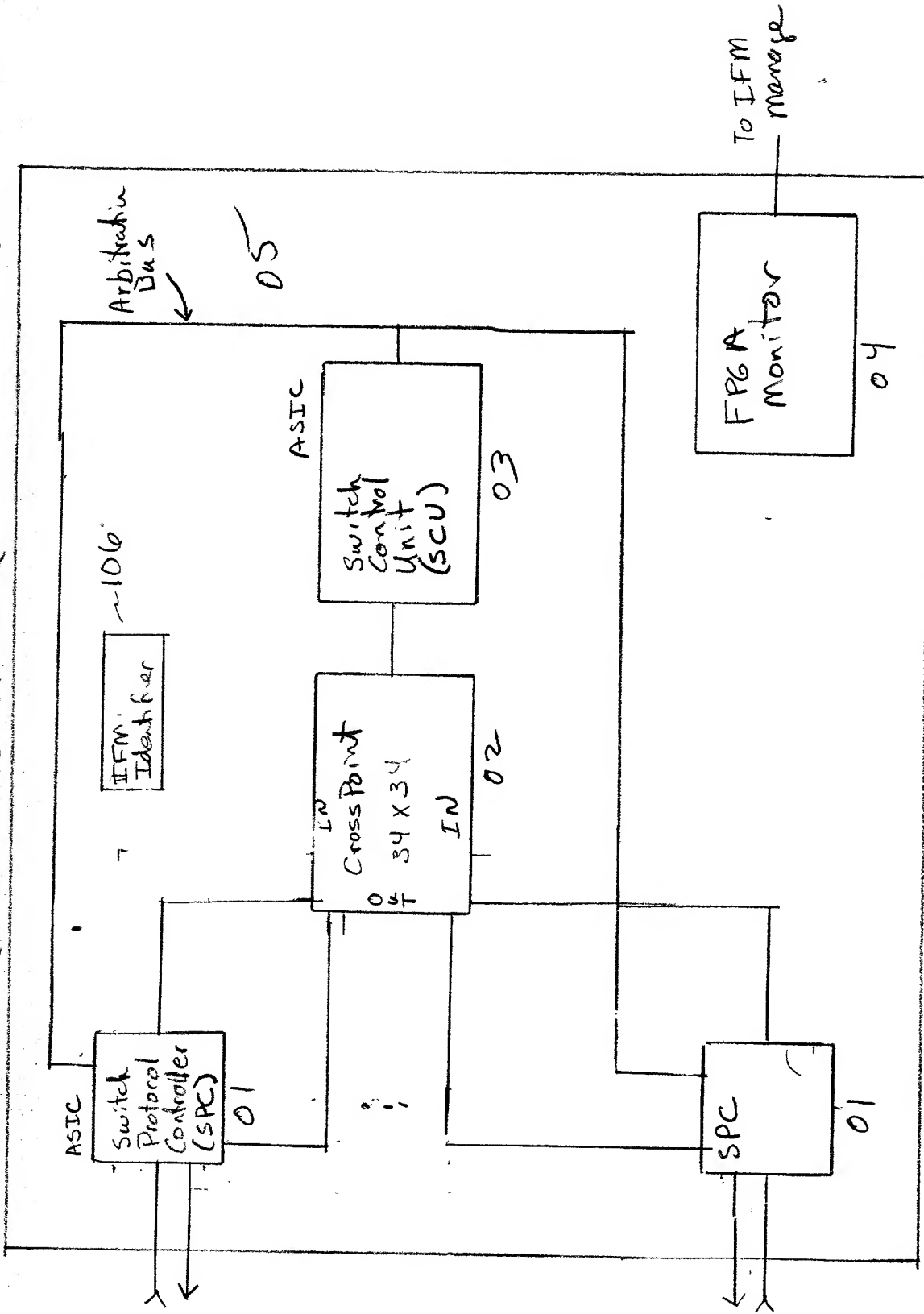


Fig 1

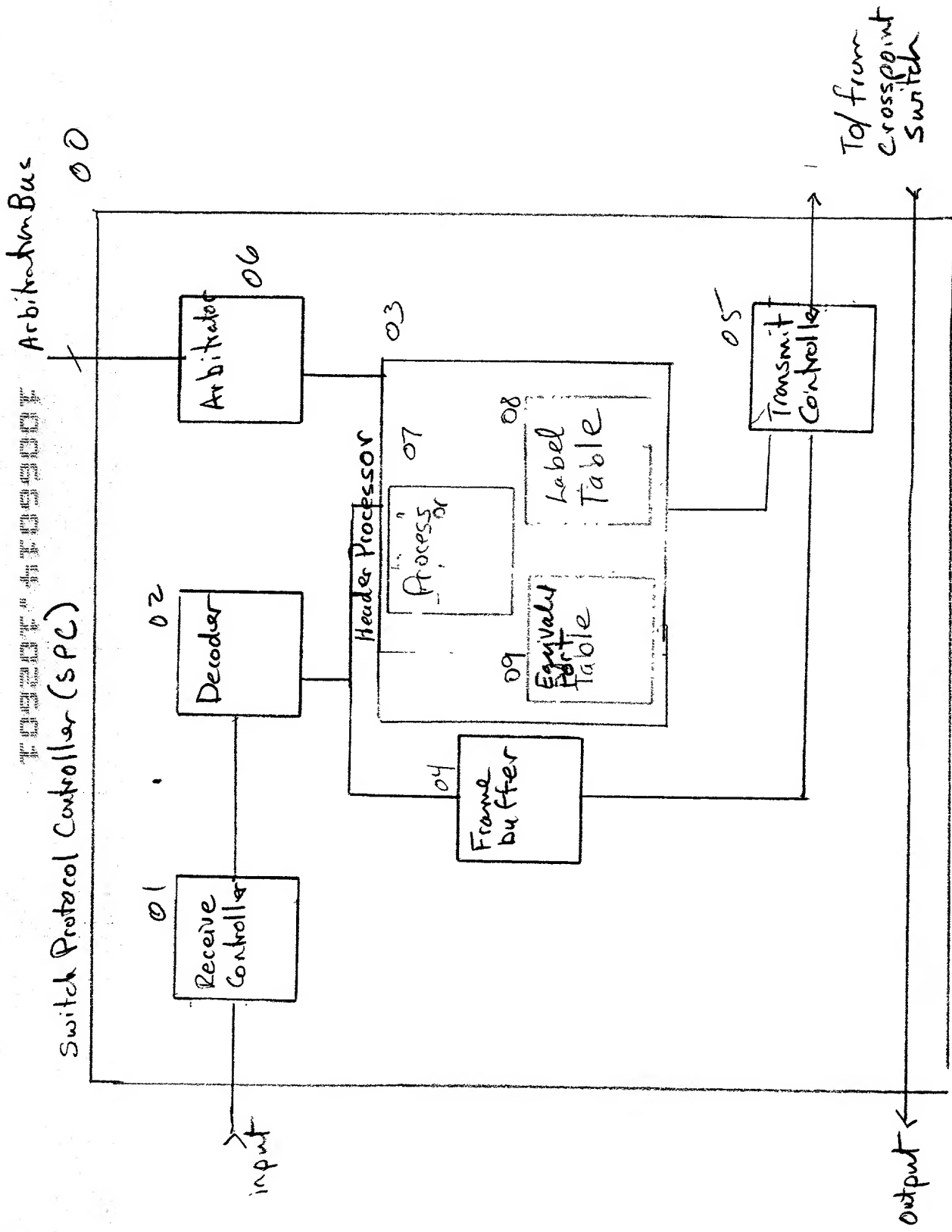


Fig 2



# Frame

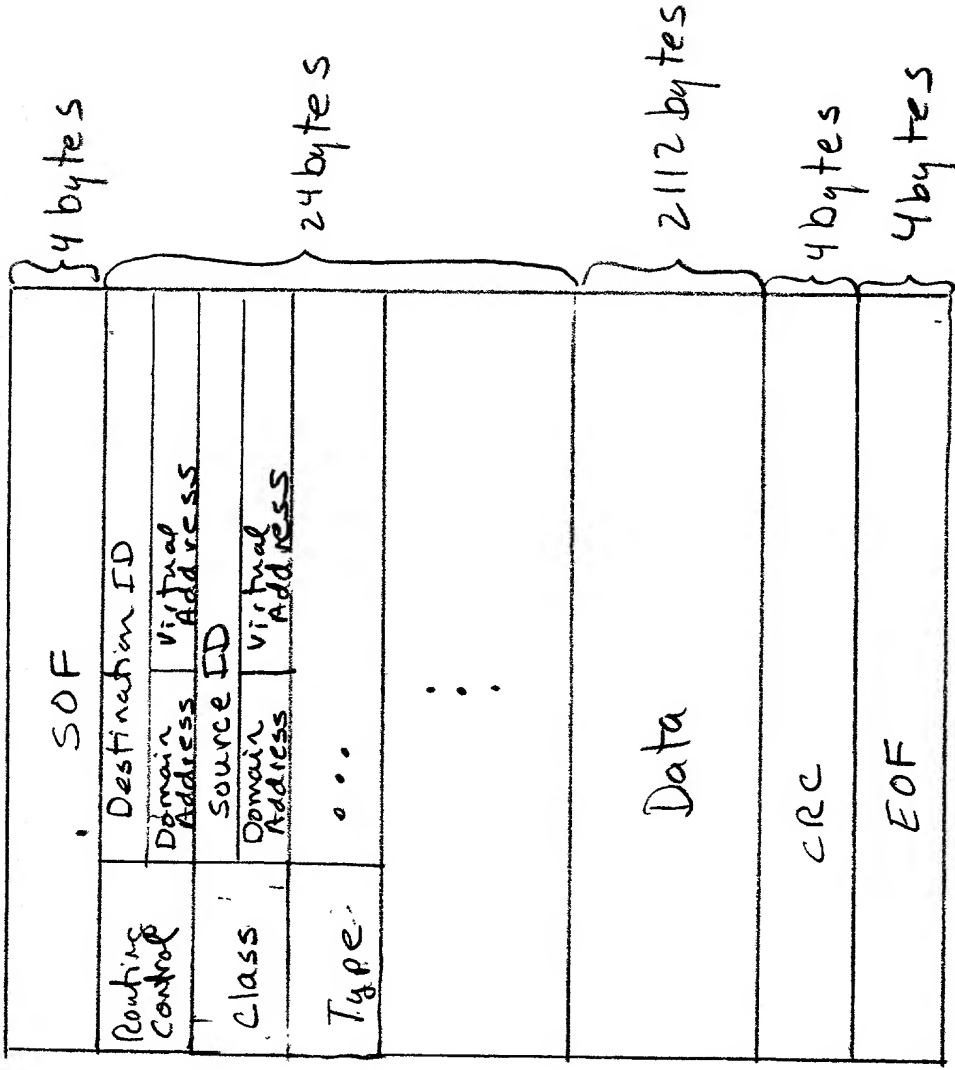


Fig 4

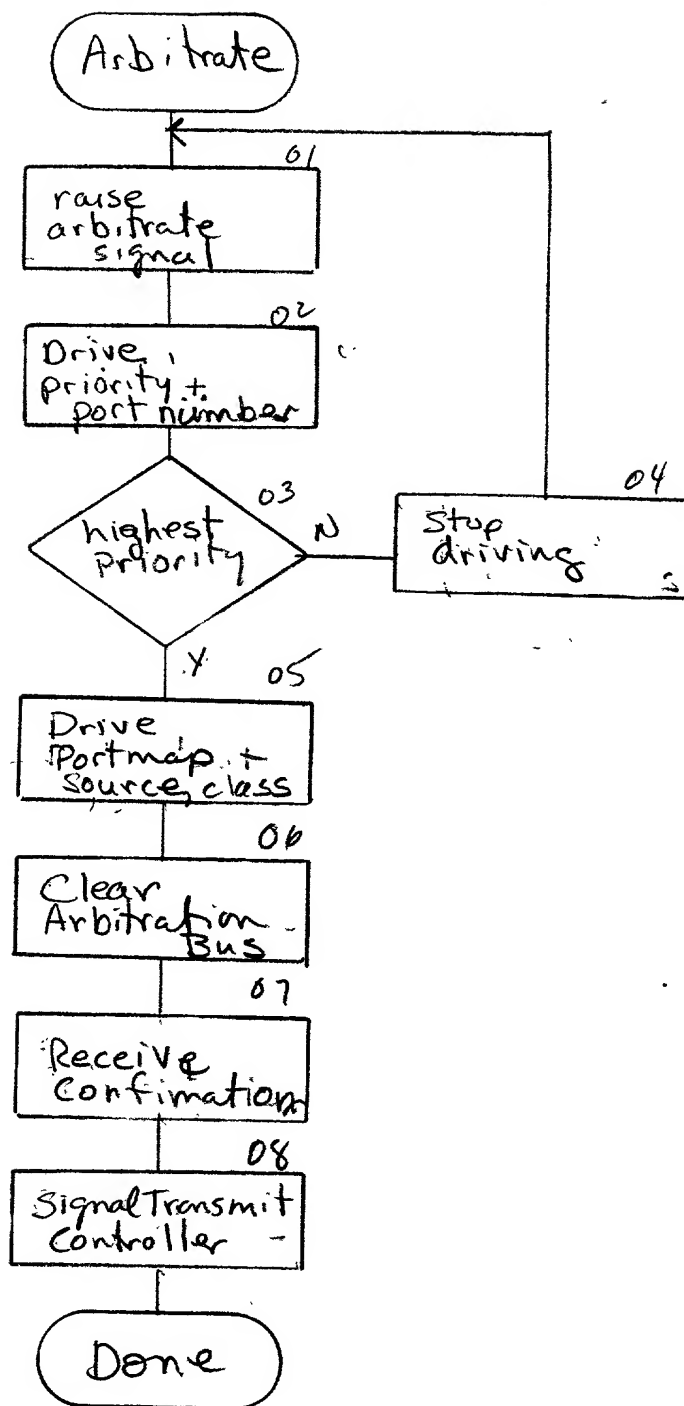
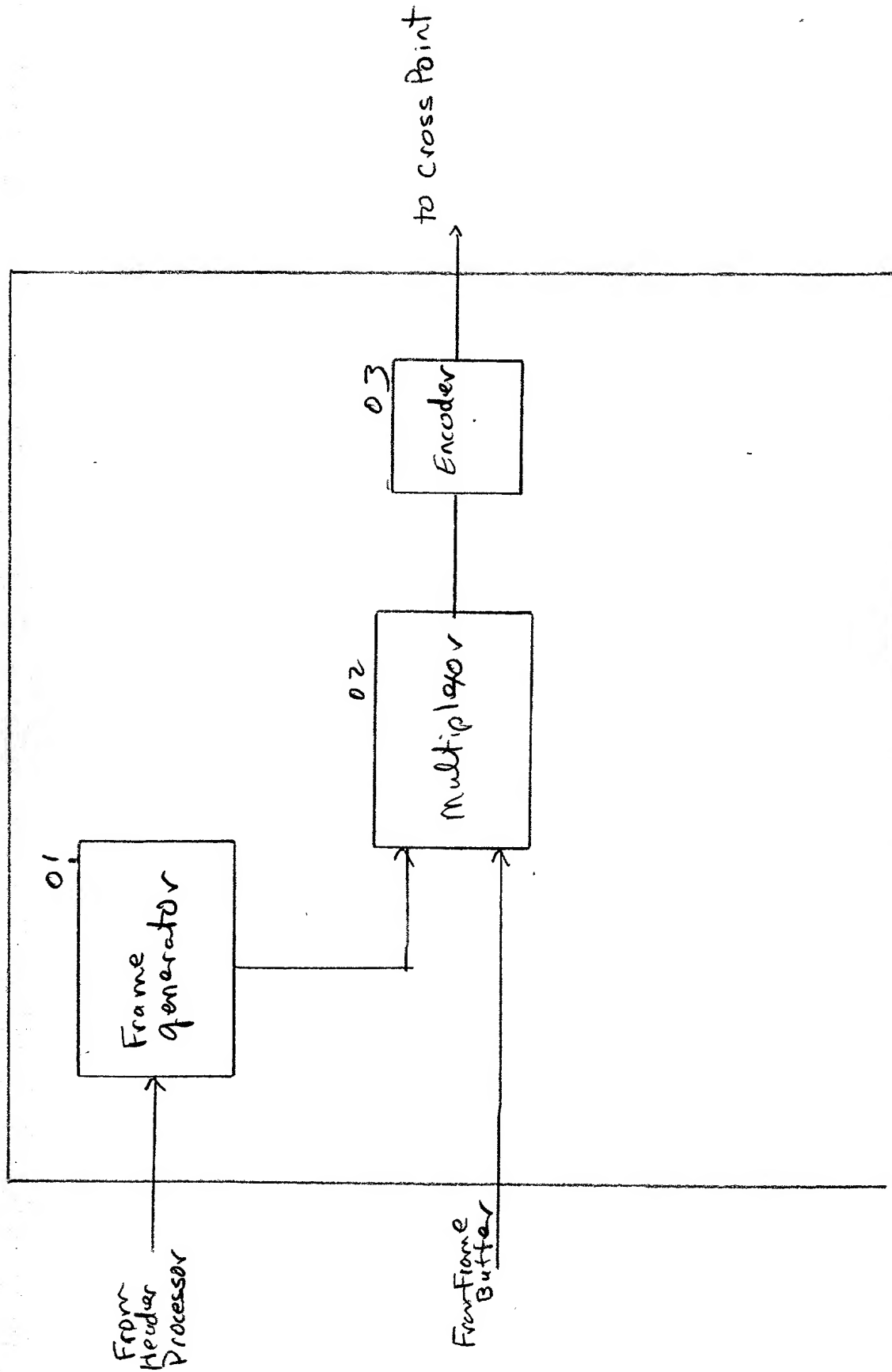


Fig 5

# Transmission Controller



F.86

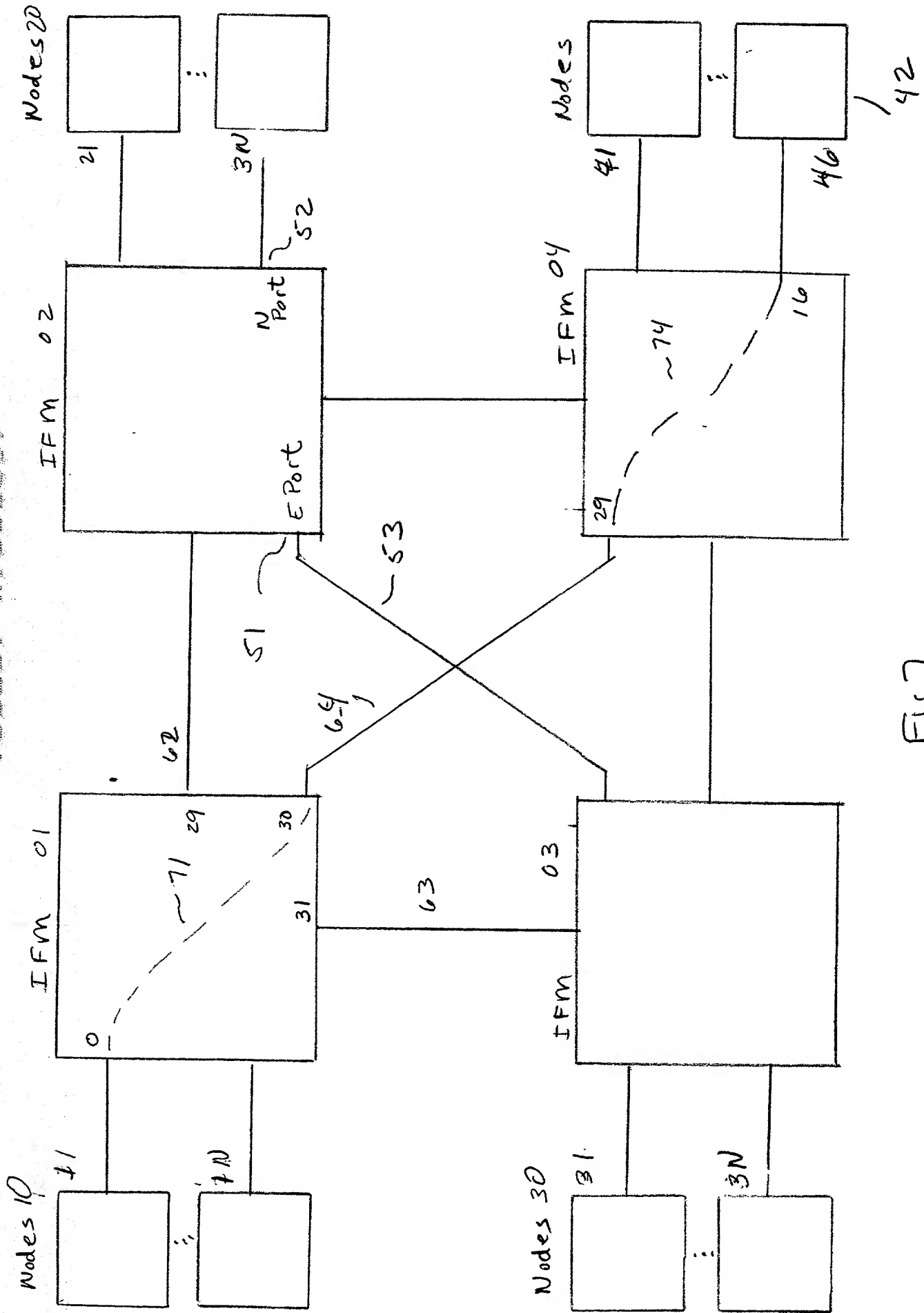


Fig 7

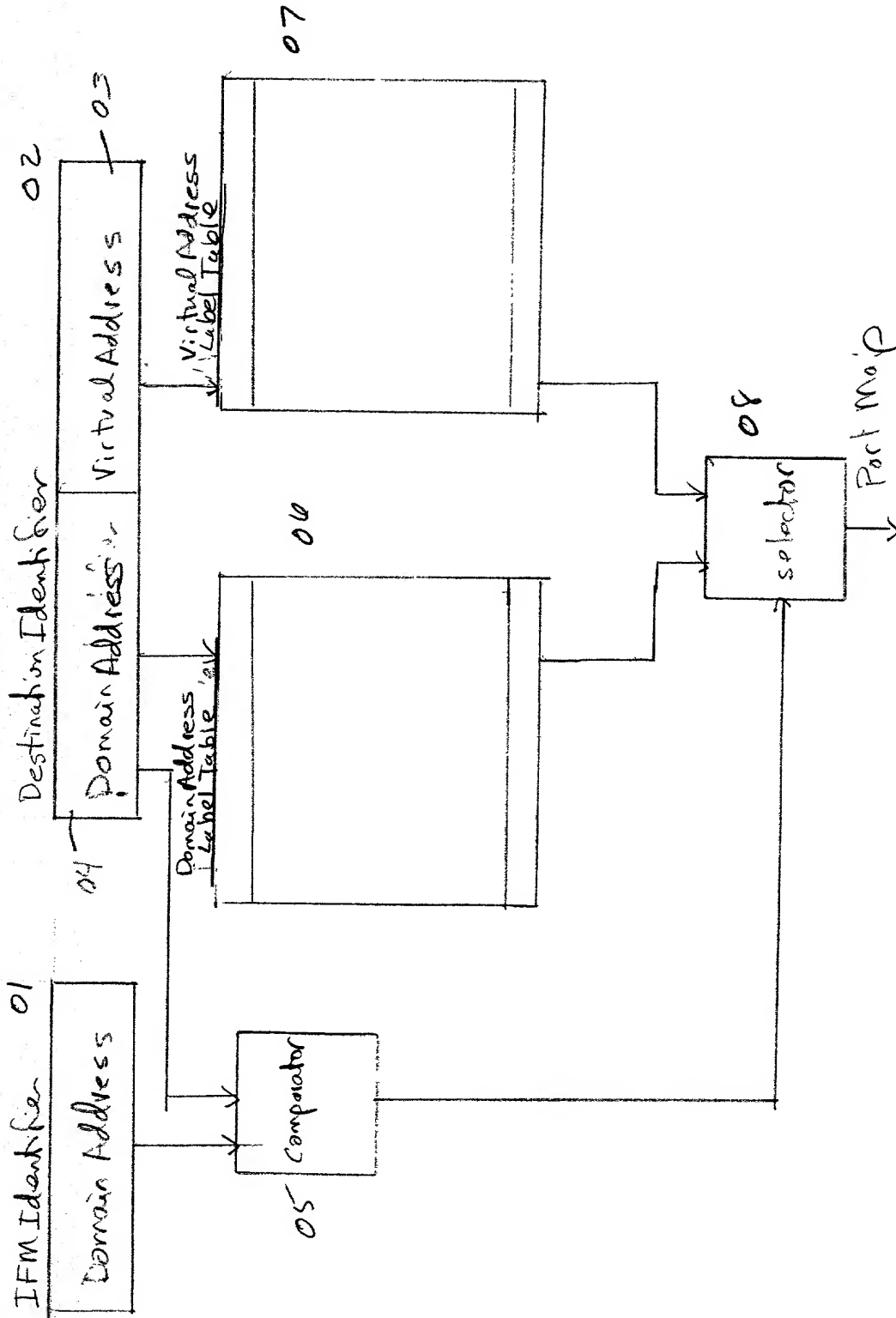


Fig. 8





00

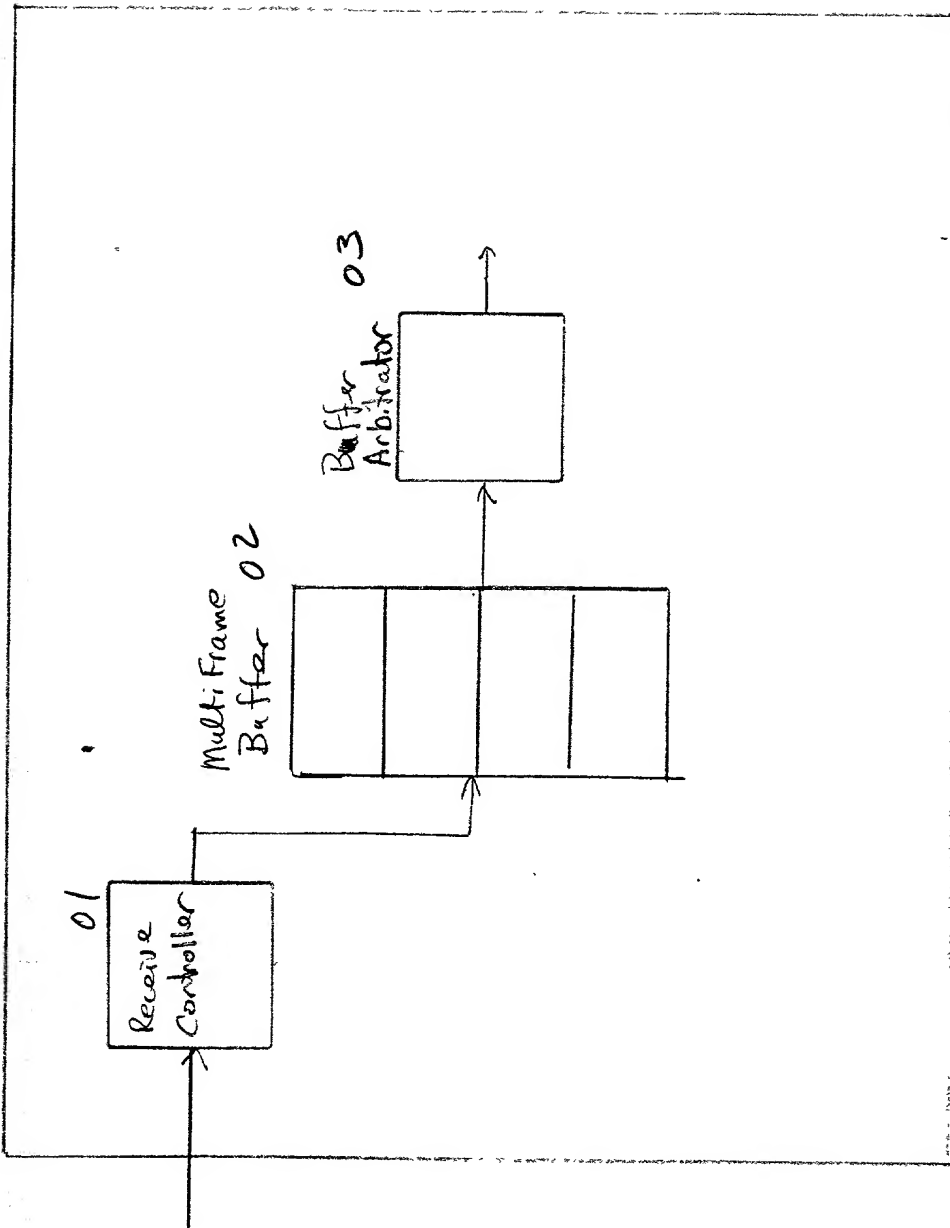


FIG 10

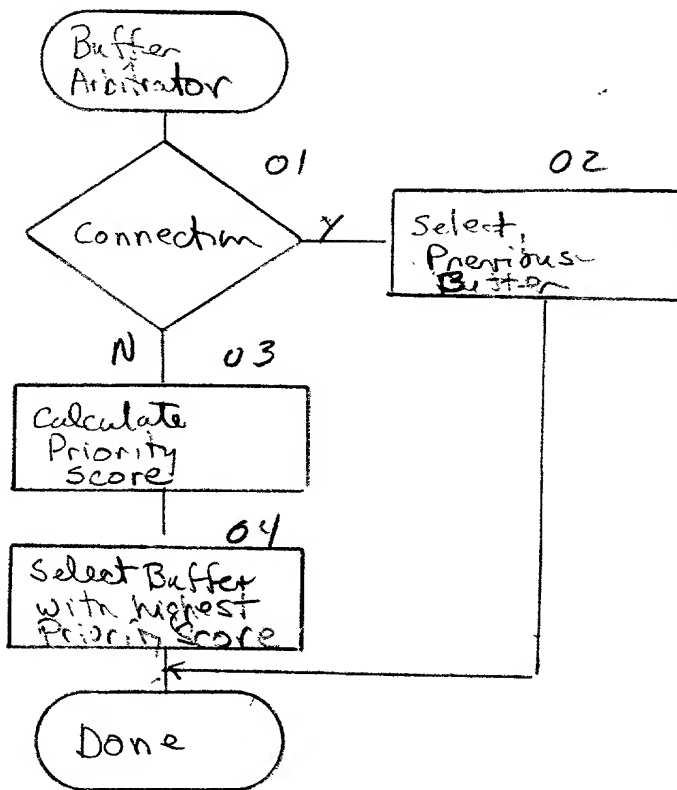


Fig 11

FIG. 12 is a block diagram of a system for processing data from a plurality of nodes.

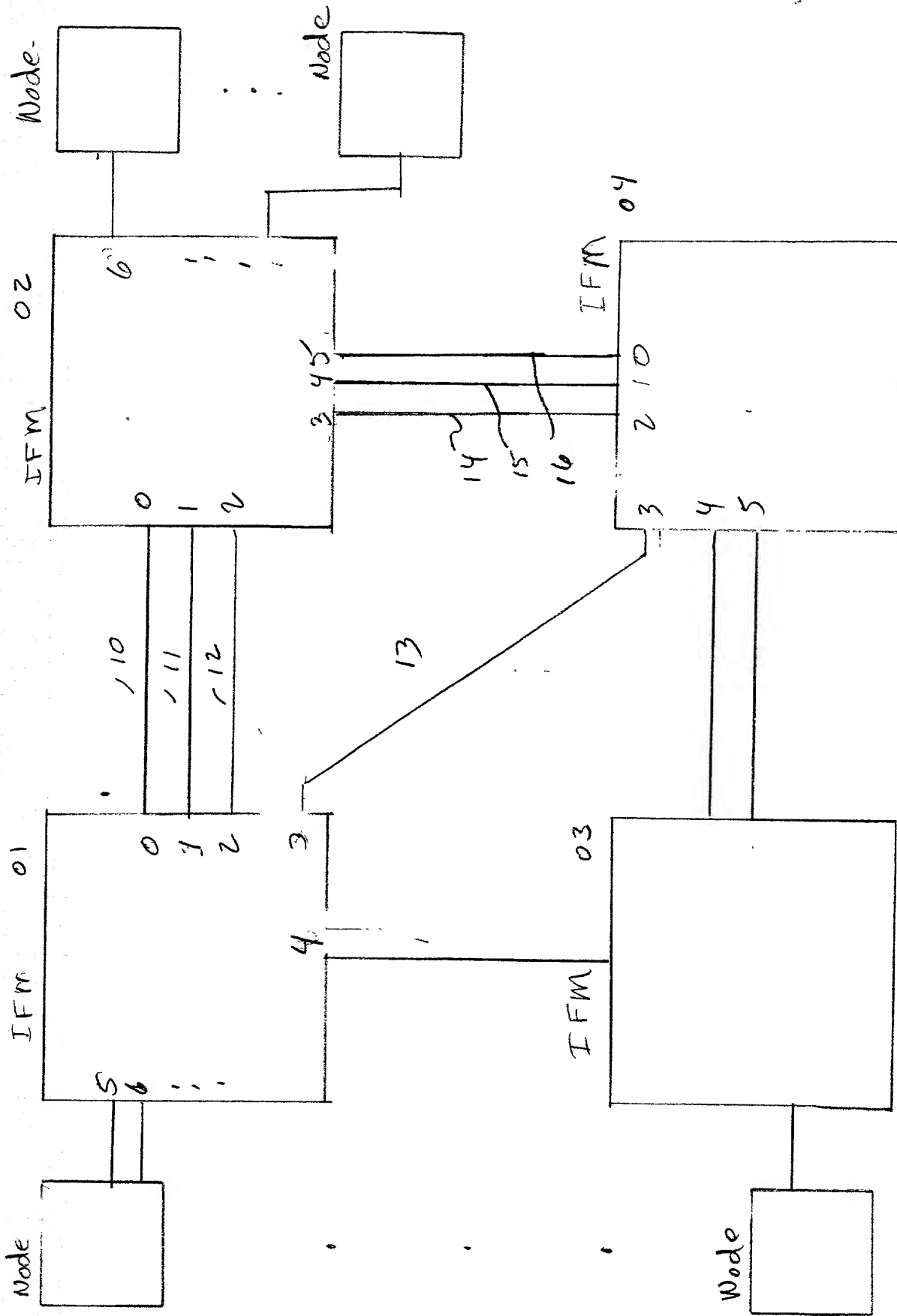


Fig 12

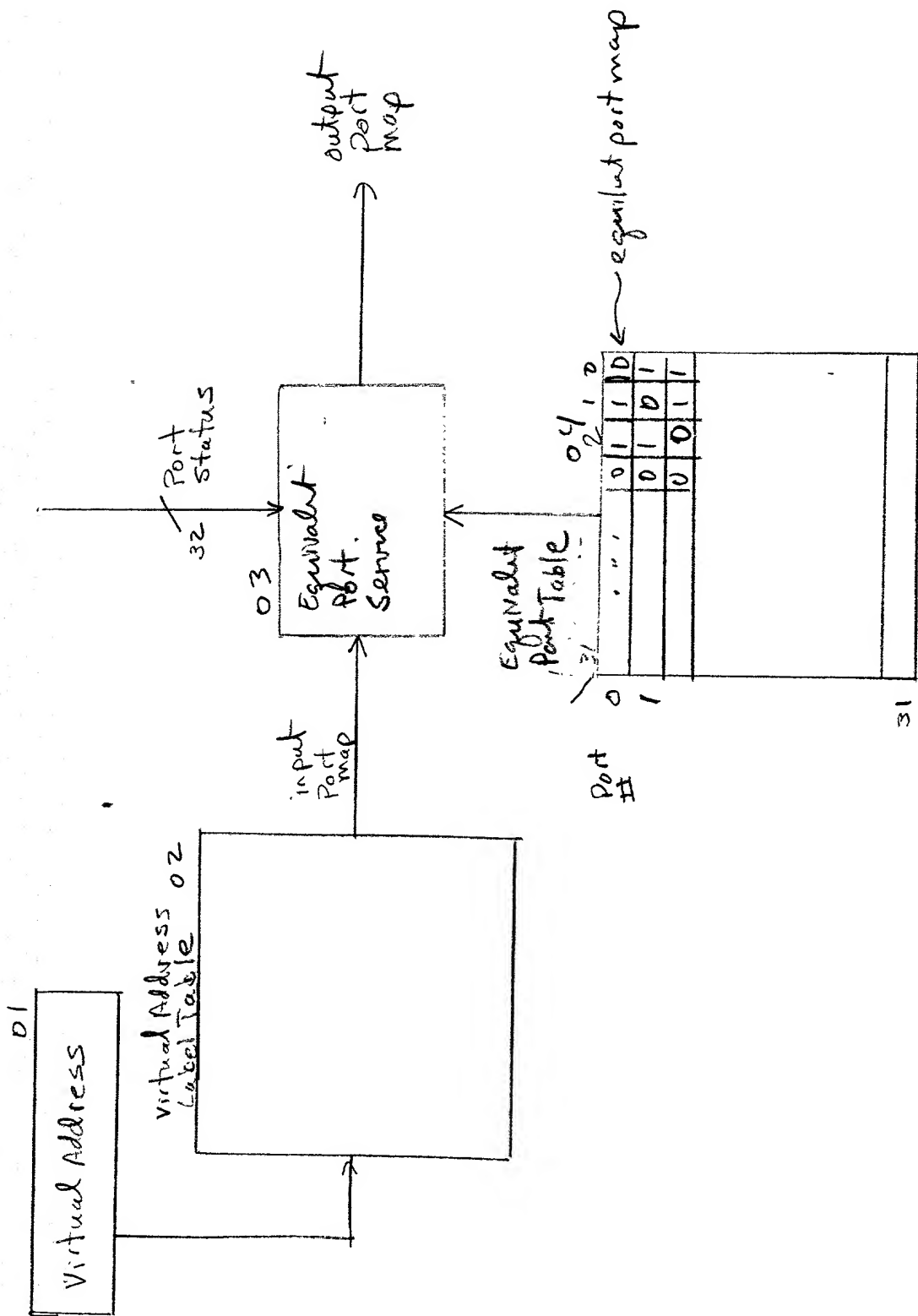


Fig 13

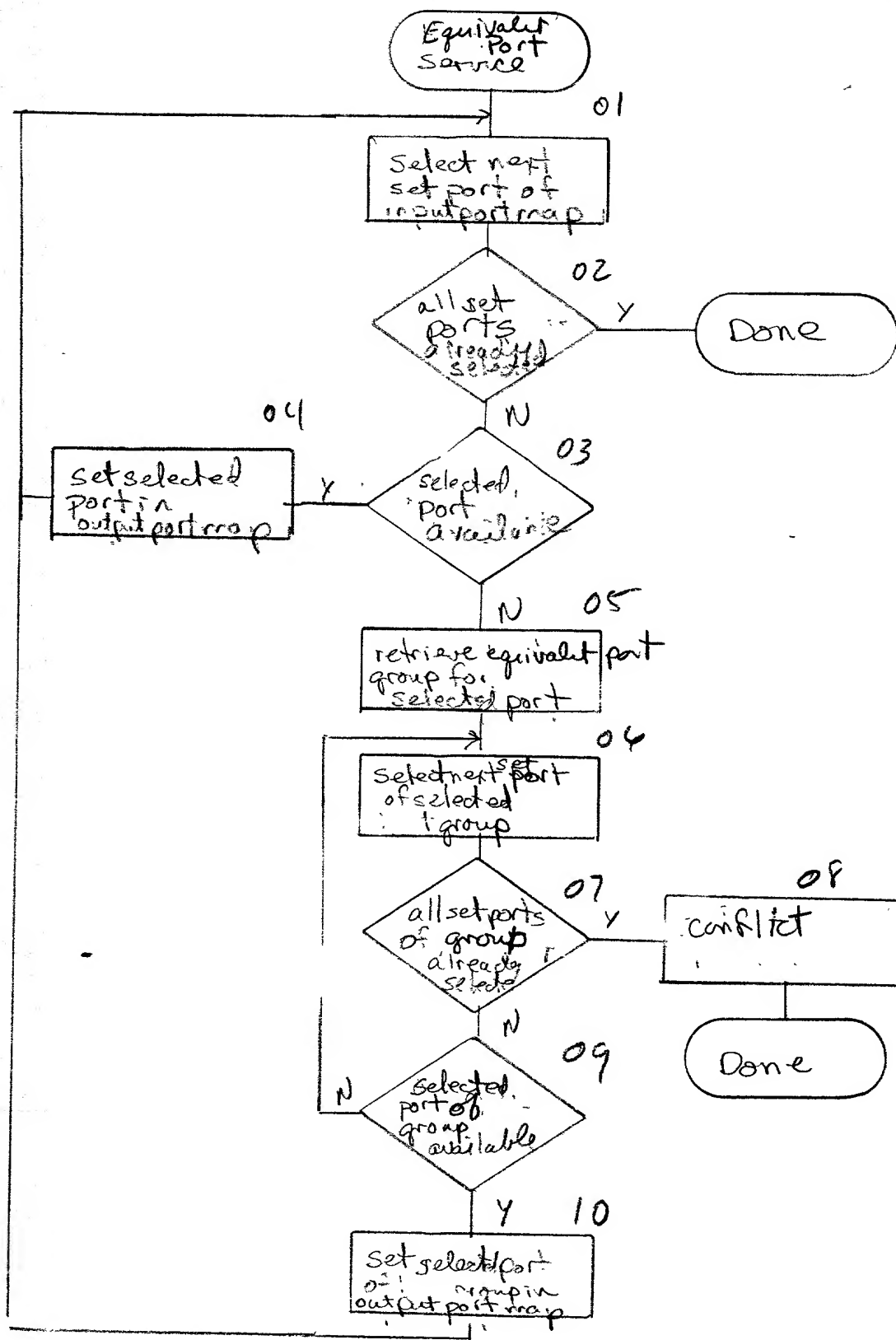


Fig 14

Header Processor

Destination Identifier

Virtual Address

01

any SPK

03

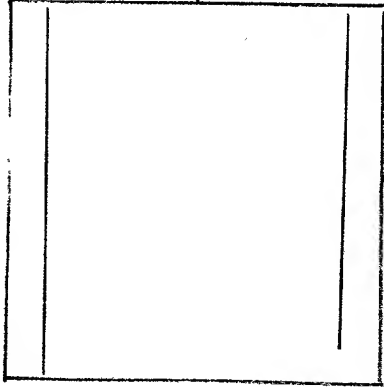
Comparator

enable port 32

enable port 33

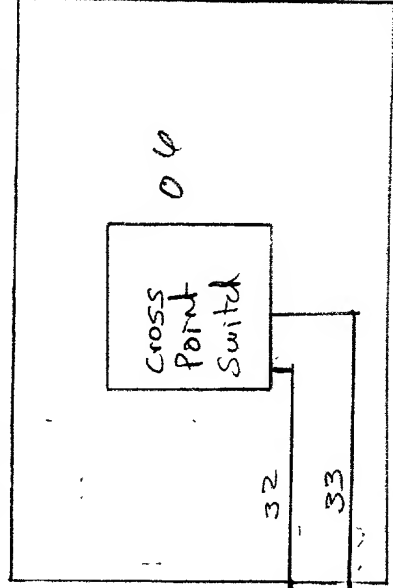
Reserved  
virtual Address Table

02



IFM

05



04

Cross  
Point  
Switch

32

33

07

IFM Manager

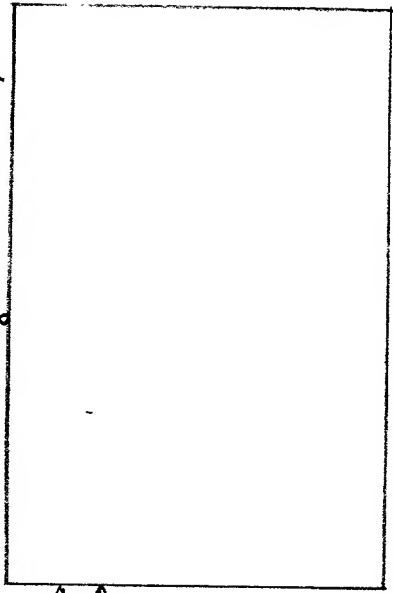
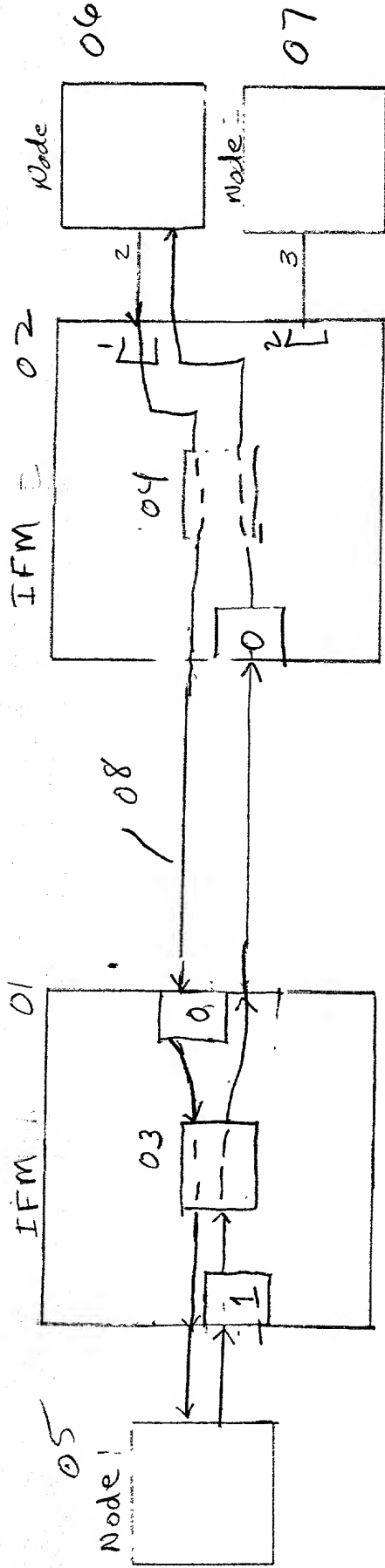


Fig 15



Dead lock

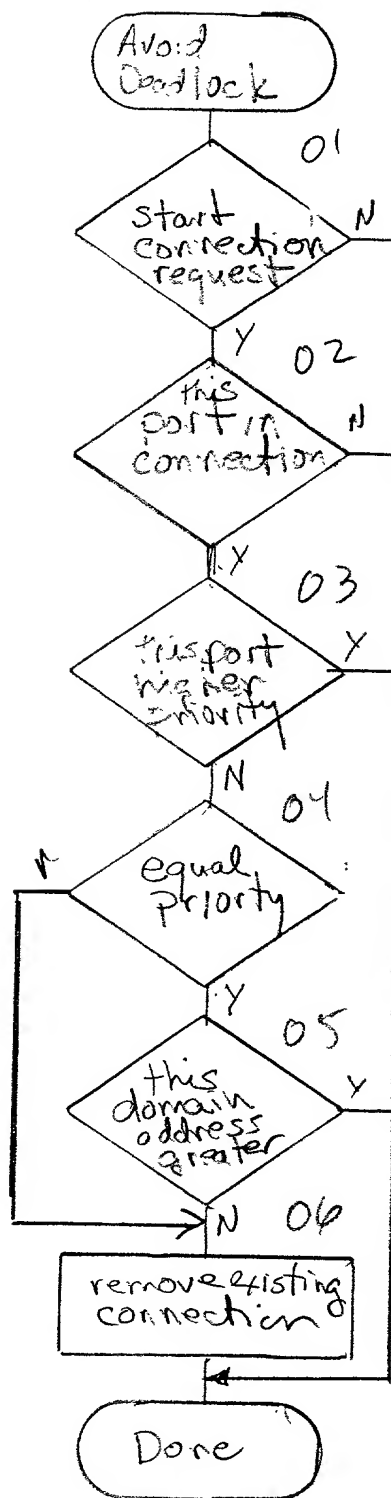
10

Time Node 1605 IFM 1601 Node 1606 IFM 1602  
 0 Send start connect 1 1 2 Send start connect 1 1 2

1	Connect 1 ↔ 0	Connect 2 ↔ 0
2	Forward start Connect 1	Forward start Connect 1
3	Send forward start connect Node 1	Can't forward start connect

Fig 16





Water end-to-end  
not established

Fig 17



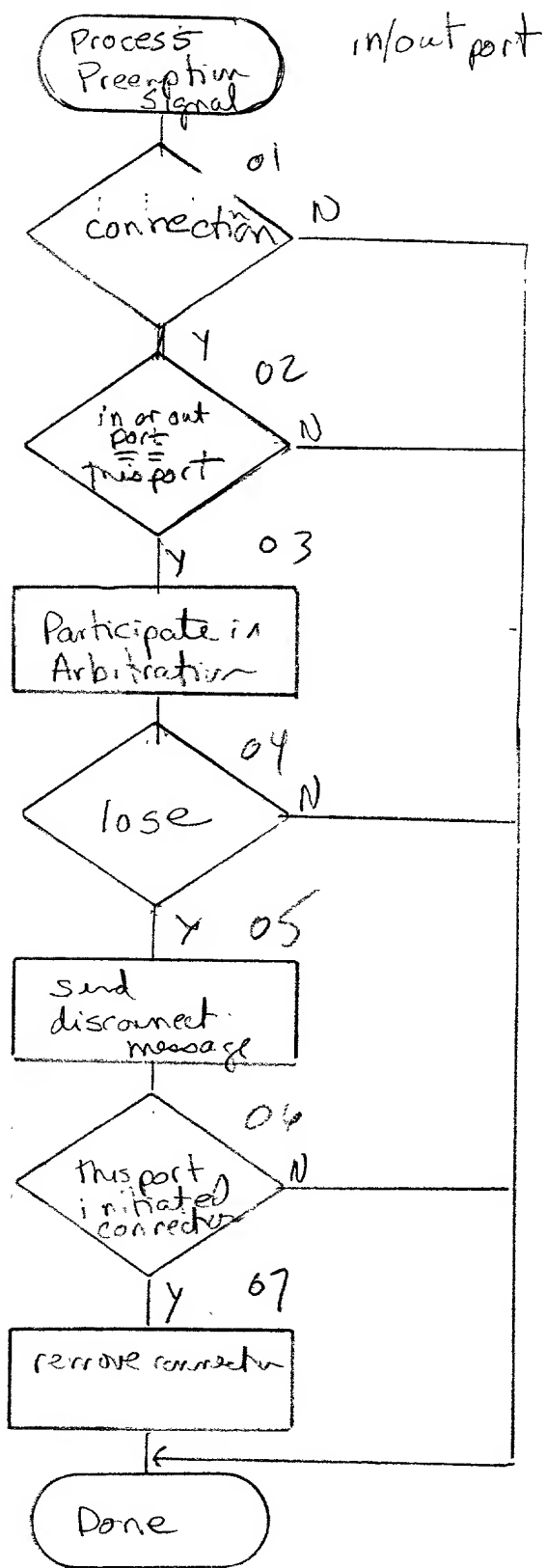


Fig. 19

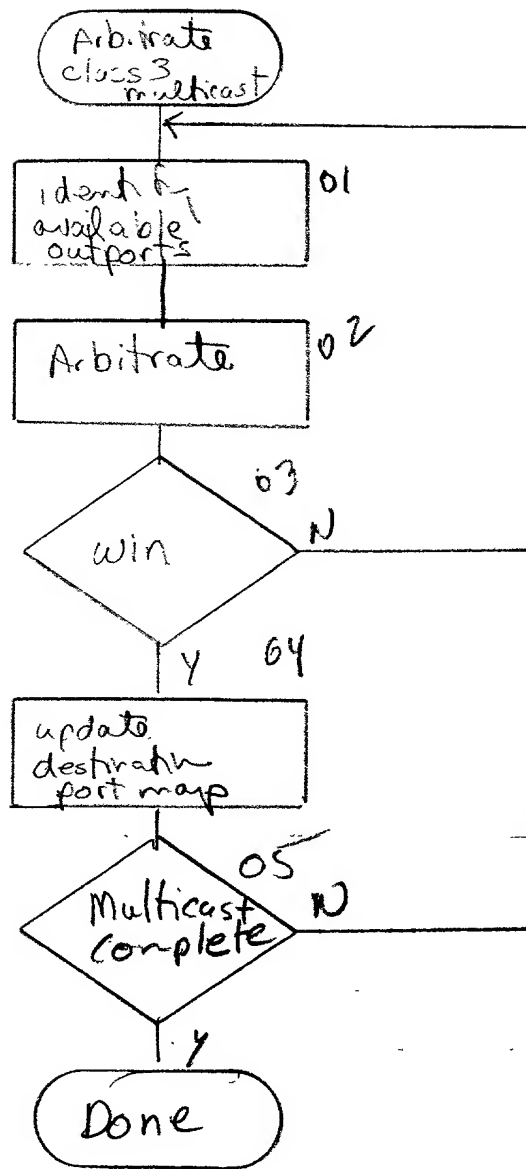


Figure 20